

	Type	L #	Hits	Search Text	DBs
1	IS&R	L1	1276	((345/864) or (345/771) or (345/776) or (345/716) or (345/717) or (345/718) or (345/863) or (345/963) or (345/727) or (345/978) or (345/839)).CCLS.	USPA T; US-P GPUB
2	IS&R	L2	2227	((379/90.01) or (379/93.09) or (379/93.05) or (379/93.17) or (379/93.19) or (379/93.23) or (379/110.01) or (379/419) or (379/441). or (379/442) or (379/456)).CCLS.	USPA T; US-P GPUB
3	IS&R	L3	1859	((455/74) or (455/556) or (455/557) or (455/566) or (455/575)).CCLS.	USPA T; US-P GPUB
4	BRS	L4	5303	1 or 2 or 3	USPA T; US-P GPUB
5	BRS	L5	279371	interact\$10.ab. or interact\$10.ti. or interfac\$7.ab. or interfac\$7.ti. or GUI\$5.ab. or GUI\$5.ti. or UI\$5.ab. or UI\$5.ti. or PDA\$5.ab. or PDA\$5.ti. or panel\$5.ab. or panel\$5.ti. or console\$5.ab. or console\$5.ti. or palm\$8.ab. or palm\$8.ti.	USPA T; US-P GPUB
6	BRS	L6	1603	4 and 5	USPA T; US-P GPUB

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 Updated
 2 Feb 2004

	Type	L #	Hits	Search Text	DBs
7	BRS	L8	201779	telephon\$8.ab. or telephon\$8.ti. or cell\$6.ab. or cell\$6.ti. or phone\$5.ab. or phone\$5.ti. or call\$5.ab. or call\$5.ti. or dial\$4.ab. or dial\$4.ti.	USPA T; US-P GPUB
8	BRS	L9	16708	5 and 8	USPA T; US-P GPUB
9	BRS	L10	802	4 and 9	USPA T; US-P GPUB
10	BRS	L14	146352	processor\$5.ab. or processor\$5.ti. or bus.ab. or bus.ti. or architect\$8.ab. or architect\$8.ti. or CPU\$5.ab. or CPU\$5.ti. or microprocessor\$5.ab. or microprocessor\$5.ti. or peripheral\$5.ab. or peripheral\$5.ti.	USPA T; US-P GPUB
11	BRS	L17	194278	host\$5.ab. or host\$5.ti. or controller\$5.ab. or controller\$5.ti. or supervis\$7.ab. or supervis\$7.ti. or slot\$5.ab. or slot\$5.ti. or card\$5.ab. or card\$5.ti.	USPA T; US-P GPUB
12	BRS	L18	242	10 and (14 or 17)	USPA T; US-P GPUB

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